

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Lu, et al.

Serial No.: 09/296,588

Group Art Unit: 2871

Filed: April 23, 1999

Examiner: Unknown

For: METHODS OF REDUCING UNBALANCED DC VOLTAGE BETWEEN TWO
ELECTRODES OF REFLECTIVE LIQUID CRYSTAL DISPLAY BY THIN FILM
PASSIVATION

Assistant Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith are five (5) sheets of formal drawings comprising Figs. 1-9 for the
above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please
substitute these formal drawings for the drawings which were filed with the application.

Respectfully Submitted,

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Date: 7/2/01

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2860
#5 drawings
7/10/01
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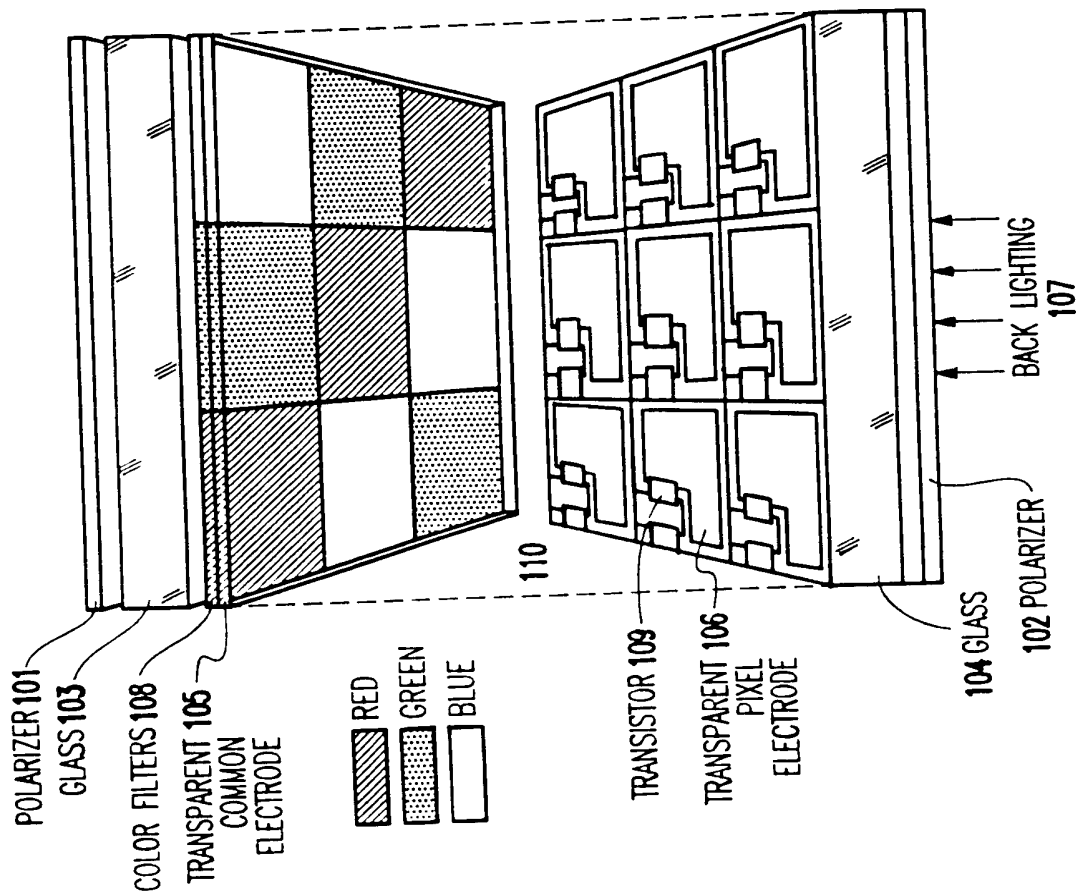


FIG. 1

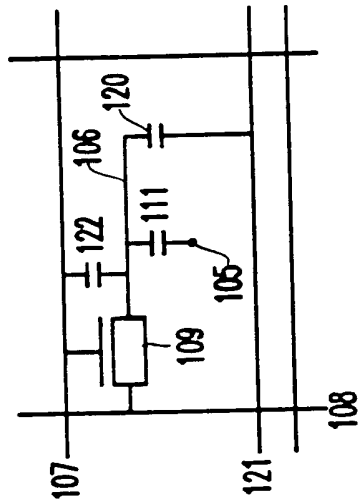


FIG. 2

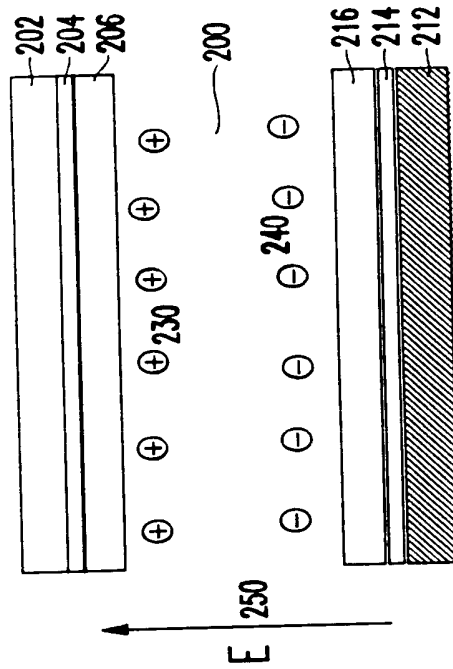
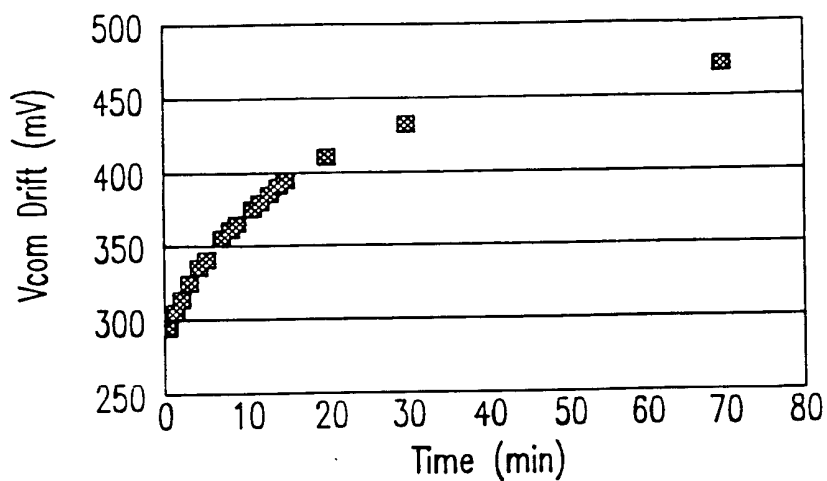
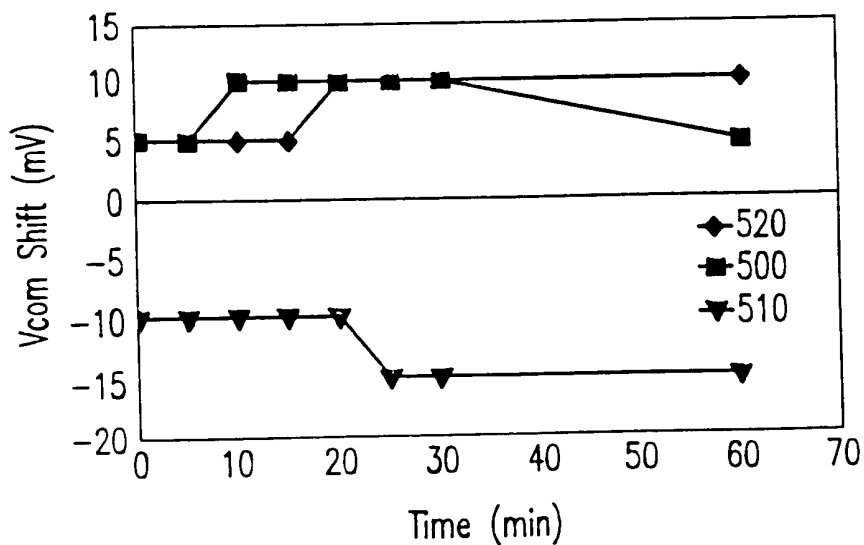


FIG. 3



Vcom shift vs. time of prior art light valves

FIG.4



Vcom shift with DLC film by different clean process

FIG.6

This cross-sectional view shows a semiconductor device with a grid of conductive pads (32) on a substrate (1). A raised structure (70) is formed on the grid. The device includes a gate stack (16) and a source/drain region (14) with a gate (20). A label 'FET' points to the transistor structure. A label '74' points to a specific feature on the raised structure.

FIG. 5B

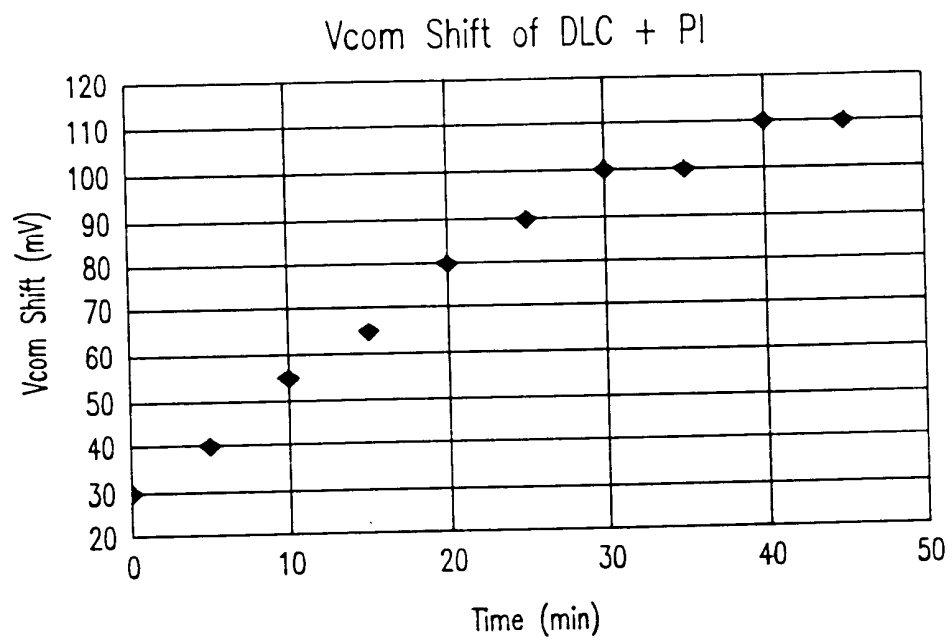


FIG.7

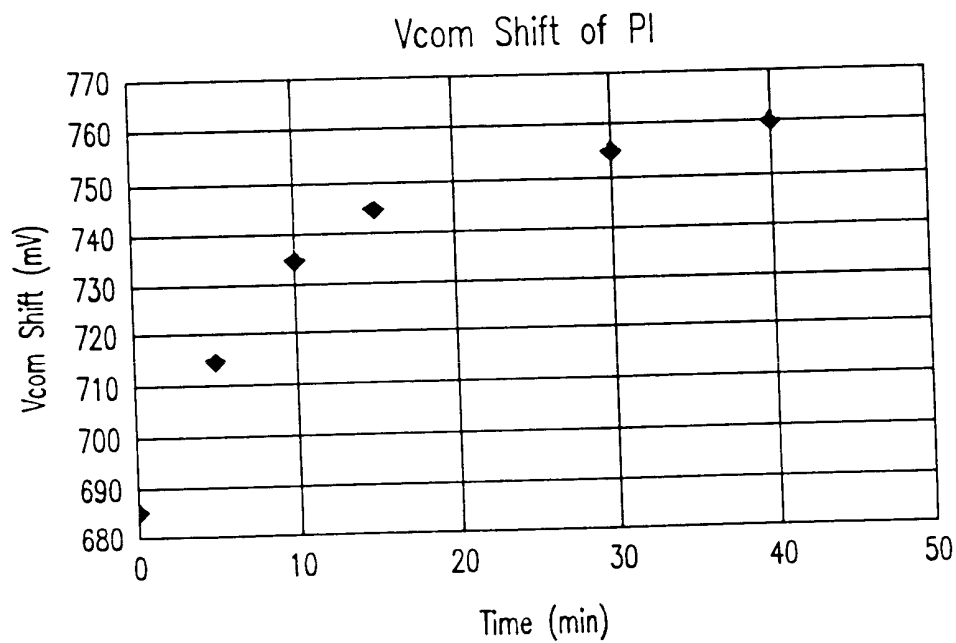


FIG.8

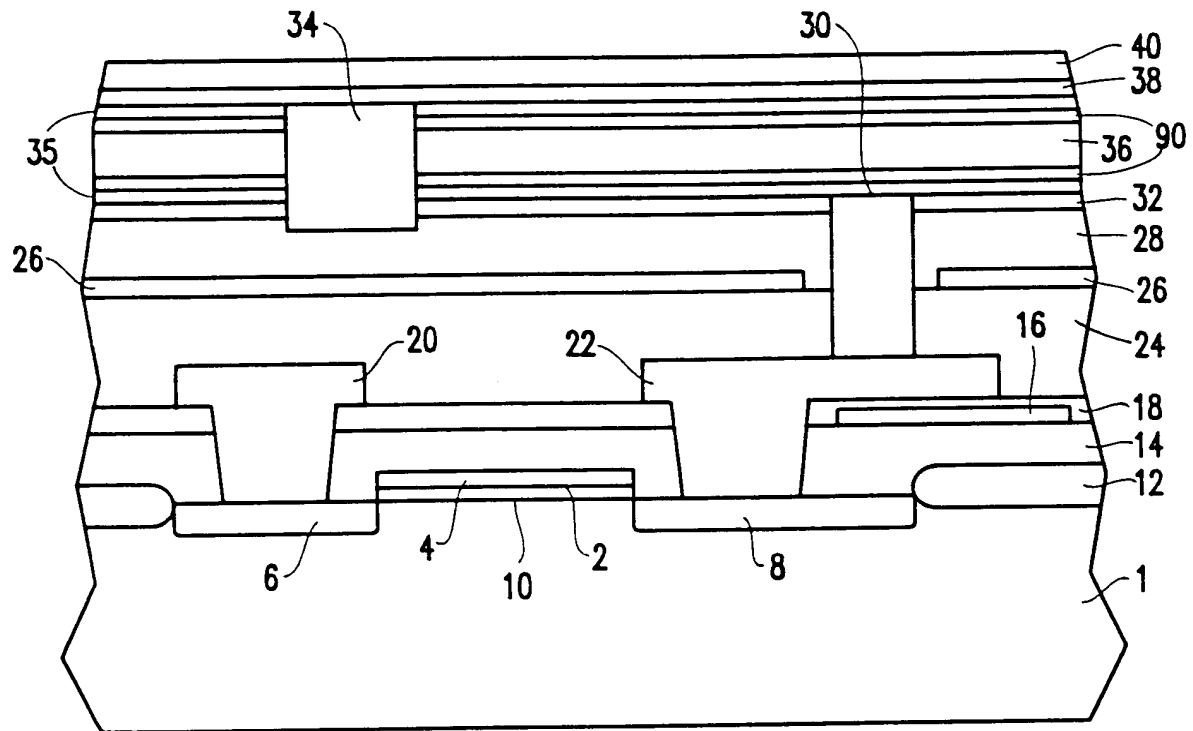


FIG.9